# **5-Bit Differential Register**

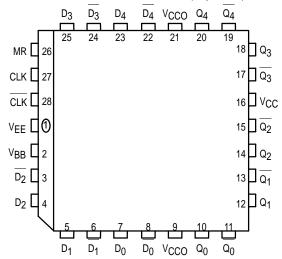
The MC10E/100E452 is a 5-bit differential register with differential data (inputs and outputs) and clock. The registers are triggered by a positive transition of the positive clock (CLK) input. A high on the Master Reset (MR) asynchronously resets all registers so that the Q outputs go LOW.

The differential input structures are clamped so that the inputs of unused registers can be left open without upsetting the bias network of the device. The clamping action will assert the D and the CLK sides of the inputs. Because of the edge triggered flip-flop nature of the device simultaneously opening both the clock and data inputs will result in an output which reaches an unidentified but valid state. Note that the input clamps only operate when both inputs fall to 2.5V below  $V_{\rm CC}$ .

The fully differential design of the device makes it ideal for very high frequency applications where a registered data path is necessary.

- Differential D, CLK and Q; VBB Reference Available
- 1100MHz Min. Toggle Frequency
- · Asynchronous Master Reset
- Extended 100E V<sub>EE</sub> Range of 4.2V to 5.46V

## Pinout: 28-Lead PLCC (Top View)



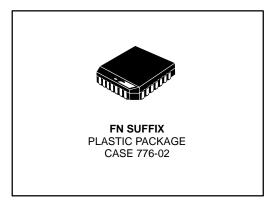
 $^{\ast}$  All VCC and VCCO pins are tied together on the die.

## **PIN NAMES**

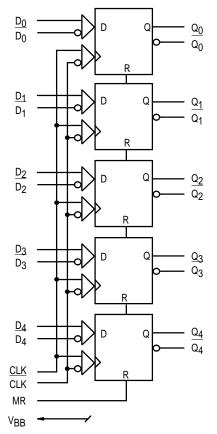
Pin	Function
D[0:4], D[0:4]	Differential Data Inputs
MR	Master Reset Input
CLK, CLK	Differential Clock Input
VBB	VBB Reference Output
Q[0:4], Q[0:4]	Differential Data Outputs

## MC10E452 MC100E452

# 5-BIT DIFFERENTIAL REGISTER



## **LOGIC DIAGRAM**





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## **DC CHARACTERISTICS** (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		–40°C		0°C		25°C			85°C						
Symbol	Characteristic	Min	Тур	Max	Unit	Cond									
V <sub>BB</sub>	Output Reference Voltage 10E 100E	-1.43 -1.38		-1.30 -1.26	-1.38 -1.38		-1.27 -1.26	-1.35 -1.38		-1.25 -1.26	-1.31 -1.38		-1.19 -1.26	V	
liH	Input HIGH Current			150			150			150			150	μΑ	
IEE	Power Supply Current 10E 100E		74 74	89 89		74 74	89 89		74 74	89 89		74 85	89 102	mA	
VCMR	Common Mode Range	-2.0		-0.4	-2.0		-0.4	-2.0		-0.4	-2.0		-0.4	V	1

V<sub>CMR</sub> is referenced to the most positive side of the differential input signal. Normal specified operation is obtained when the input signals are within the V<sub>CMR</sub> range and the input swing is greater than V<sub>PP</sub>.

## **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

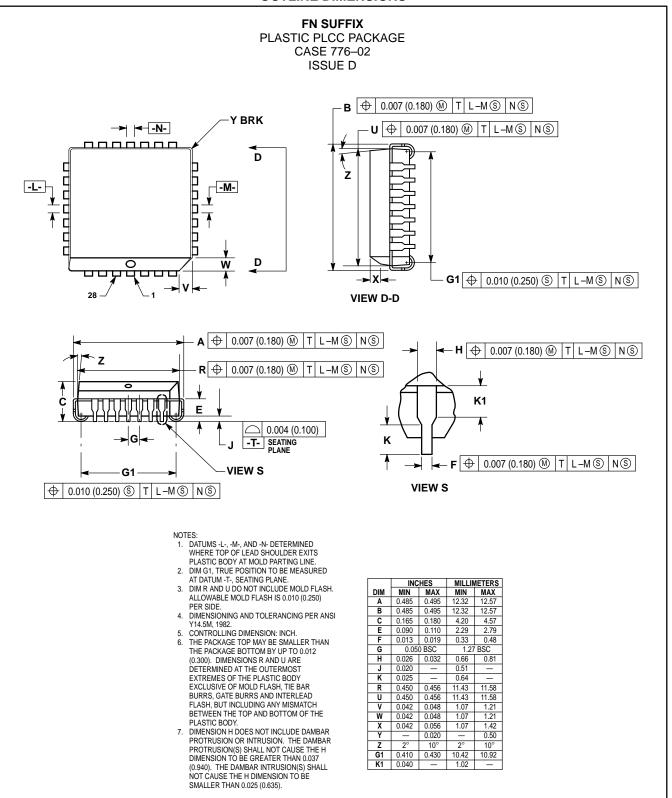
			–40°C		O	°C to 85°0				
Symbol	Characteristic		Min	Тур	Max	Min	Тур	Max	Unit	Condition
fMAX	Maximum Toggle Frequency		1000	1400		1100	1400		MHz	
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output	CLK (Diff) CLK (SE) MR	425 375 375	600 600 625	850 900 900	475 425 425	600 600 625	800 850 850	ps	
ts	Setup Time	D	175	-50		150	-50		ps	
tH	Hold Time	D	225	50		200	50		ps	
tRR	Reset Recovery Time		750	450		700	450			
tpW	Minimum Pulse Width	CLK MR	400 400			400 400			ps	
tskew	Within-Device Skew			50			50		ps	1
VPP	Minimum Input Swing		150			150			mV	2
t <sub>r</sub> /t <sub>f</sub>	Rise/Fall Times		250	475	725	275	475	675	ps	20–80%

<sup>1.</sup> Within-device skew is defined as identical transitions on similar paths through a device.

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<sup>2.</sup> Minimum input swing for which AC parameters are guaranteed.

### **OUTLINE DIMENSIONS**



#### MC10E452 MC100E452

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#### How to reach us:

**USA/EUROPE/Locations Not Listed**: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

**JAPAN**: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298





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